

**IN THE CLAIMS:**

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~strikethrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

Please CANCEL claims 21-32 in accordance with the following:

1. (Previously Presented) A liquid crystal display (LCD) panel comprising:  
a liquid crystal panel in which liquid crystal is filled between upper and lower substrates and the liquid crystal is in communication with a display electrode and a common electrode which face each other;  
a first driving circuit connected to the liquid crystal panel by a plurality of data lines and which applies a data signal to the liquid crystal panel;  
a second driving circuit connected to the liquid crystal panel by a plurality of gate lines and which applies a scan signal to sequentially apply the data signal to the liquid crystal panel;  
an electrode pad unit which applies an alignment signal voltage to the liquid crystal panel for alignment of the liquid crystal filled in the liquid crystal panel;  
a first switching circuit which performs a switching operation to apply the alignment signal voltage applied via the electrode pad unit to the liquid crystal panel via the data lines;  
a second switching circuit which performs a switching operation to apply the alignment signal voltage applied via the electrode pad unit to the liquid crystal panel via the gate lines; and  
first and second buffer circuits, which prevent the alignment signal voltage from being applied to the first and second driving circuits,  
wherein the electrode pad unit is directly connected to the first switching circuit and the second switching circuit.

2. (Original) The LCD panel of claim 1, wherein the first switching circuit is placed between the first driving circuit and the liquid crystal panel.

3. (Withdrawn) The LCD panel of claim 1, wherein the second switching circuit is placed between the liquid crystal panel and the electrode pad unit.

4. (Withdrawn) The LCD panel of claim 1, wherein the second switching circuit is placed

between the second driving circuit and the liquid crystal panel.

5. (Original) The LCD panel of claim 2, wherein the first buffer circuit is placed between the first driving circuit and the first switching circuit.

6. (Withdrawn) The LCD panel of claim 3, wherein the second buffer circuit is placed between the second driving circuit and the liquid crystal panel.

7. (Withdrawn) The LCD panel of claim 4, wherein the second buffer circuit is placed between the second driving circuit and the second switching circuit.

8. (Withdrawn) The LCD panel of claim 1, wherein the first switching circuit faces the first driving circuit with the liquid crystal panel being placed between the first switching circuit and the first driving circuit.

9. (Withdrawn) The LCD panel of claim 8, wherein the second switching circuit is placed between the liquid crystal panel and the electrode pad unit.

10. (Withdrawn) The LCD panel of claim 8, wherein the second switching circuit is placed between the second driving circuit and the liquid crystal panel.

11. (Withdrawn) The LCD panel of claim 8, wherein the first buffer circuit is placed between the first driving circuit and the liquid crystal panel.

12. (Withdrawn) The LCD panel of claim 1, wherein the electrode pad unit comprises:  
first and second electrode pads connected to the first switching circuit;  
a third electrode pad connected to the common electrode; and  
fourth and fifth electrode pads connected to the second switching circuit.

13. (Withdrawn) The LCD panel of claim 1, wherein the first buffer circuit includes a plurality of signal backflow prevention elements, each connected to the respective corresponding plurality of data lines.

14. (Withdrawn) The LCD panel of claim 1, wherein the second buffer circuit includes a plurality of signal backflow prevention elements each connected to the respective corresponding plurality of gate lines.

15. (Withdrawn) The LCD panel of claim 1, wherein the first switching circuit includes a plurality of transistors, each connected to the respective corresponding data lines.

16. (Withdrawn) The LCD panel of claim 1, wherein the second switching circuit includes a plurality of transistors, each connected to the respective corresponding gate lines.

17. (Original) The LCD panel of claim 2, wherein the second switching circuit is placed between the liquid crystal panel and the electrode pad unit.

18. (Withdrawn) The LCD panel of claim 2, wherein the second switching circuit is placed between the second driving circuit and the liquid crystal panel.

19. (Previously Presented) A liquid crystal display panel, comprising:  
a liquid crystal panel comprising a plurality of pixels in liquid crystal to display images;  
a driving circuit supplying signals to the plurality of pixels to control the display images;  
an electrode unit to supply an alignment signal voltage to the liquid crystal panel;  
a switching circuit selectively switching the alignment signal voltage from the electrode unit to the liquid crystal display panel to align liquid crystal in the liquid crystal panel; and  
a buffer circuit connected to the driving circuit to prevent the alignment signal voltage from flowing to the driving circuit,

wherein the electrode pad unit is directly connected to the first switching circuit and the second switching circuit.

20. (Withdrawn) The liquid crystal display panel of claim 19, wherein the liquid crystal is ferroelectric liquid crystal.

21-32. (Cancelled)